UNIFIED REAL-TIME MICROPROCESSOR COMPUTER

ABSTRACT: A multiport revolving chambered homing binary hunting metallic track encasing hermetic data link caster dart castings constrained new software parallel redundancy cosmos robotizing unified real-time microprocessor machine language computer whose universal dominion domain outline involves automatizing real-time holistically steady state synchronized ubiquitous continuum sub-loculated cyclical parallel redundancy cosmos robotizing unified real-time microprocessor computer logic instructions of multiport revolving chambered homing binary hunting metallic track encasing hermetic data link caster dart castings constrained new end-user graphical human apostrophe interface syntactic synthesis real-time software programs block; multiport revolving chambered homing binary hunting metallic track encasing hermetic data link caster dart castings constrained new algebraic problem-solving application syntactic synthesis real-time software programs block; multiport revolving chambered homing binary hunting metallic track encasing hermetic data link caster dart castings constrained new robotizing gauge indicating guidance syntactic synthesis real-time software programs block; multiport revolving chambered homing binary hunting metallic track encasing hermetic data link caster dart castings constrained new commerce and transactional exchange methodology syntactic synthesis real-time software programs block; multiport revolving chambered homing binary hunting metallic track encasing hermetic data link caster dart castings constrained new commerce and trade languages combinatorial syntactic synthesis real-time software programs block; a complete real-time microprocessor logic instructing compact integrated originating real-time software generator AND-OR closed-circuitry microprocessor operating system block concretizing rubric identic automatizing real-time holistically steady state synchronized ubiquitous continuum universal executive microprogrammable systematic codified microprocessor machine language logic operator instructions of one-time programmable read-only memory (PROM); a multiport revolving chambered homing binary hunting metallic track encasing hardcore software accumulator controlling central processing unit (CPU).